

FIG. 1A

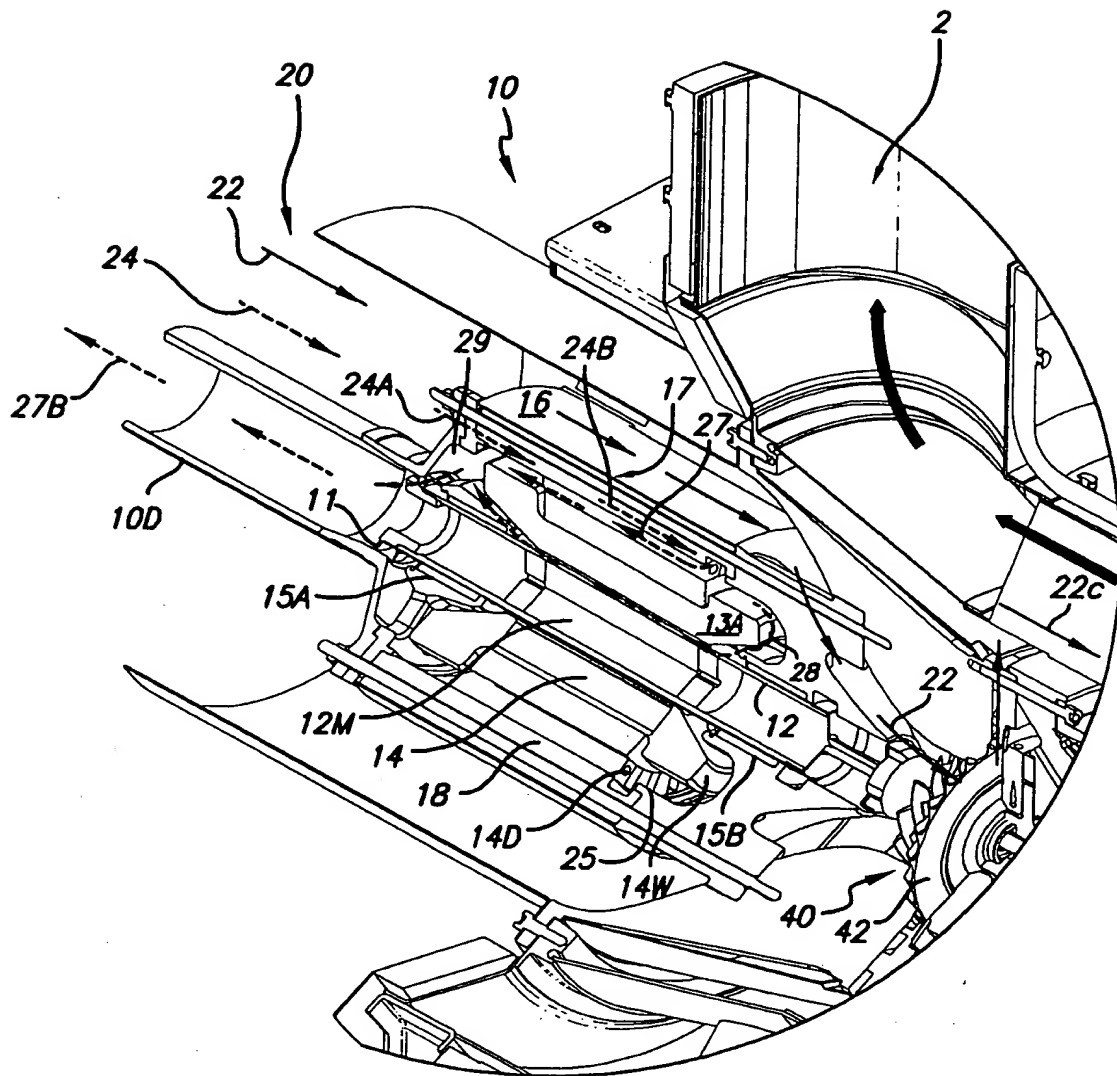


FIG. 1B



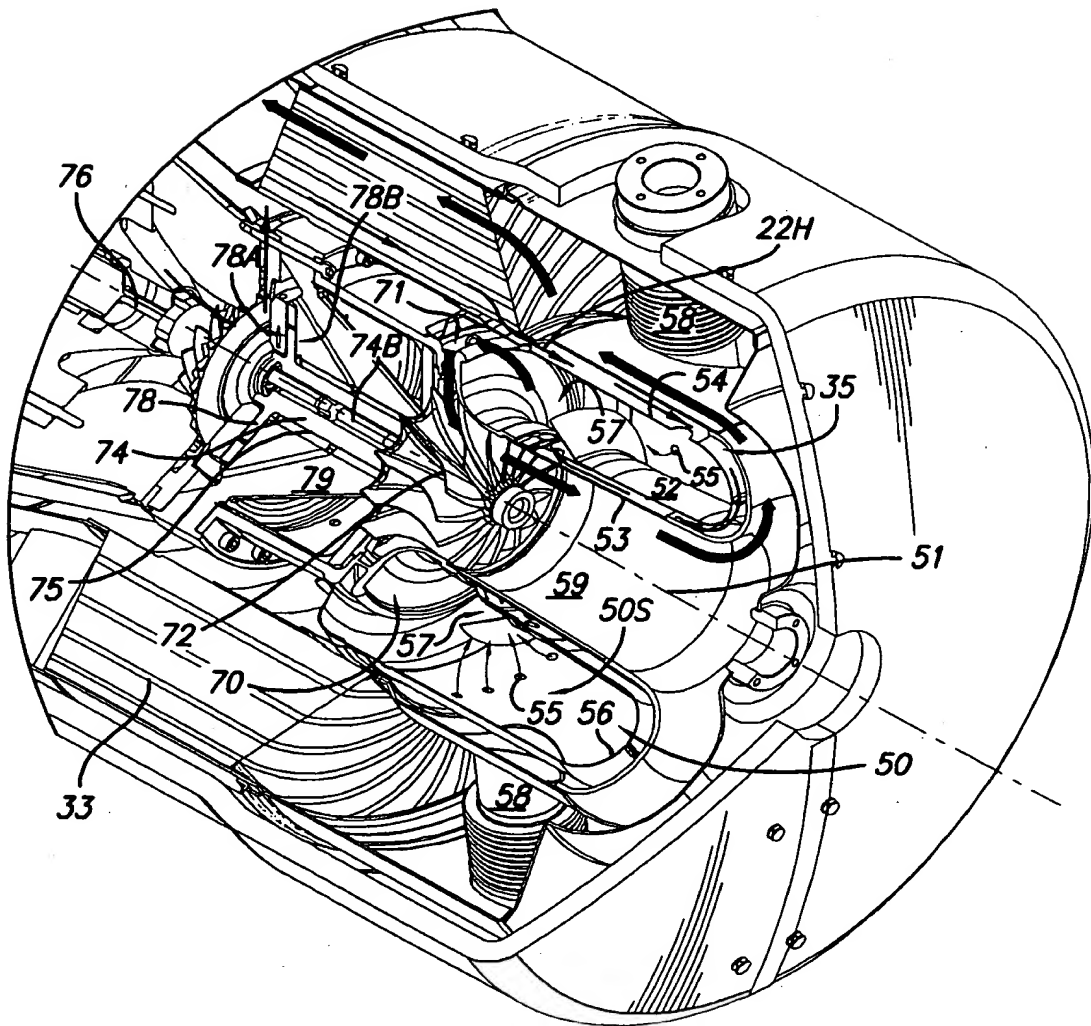


FIG. 1D



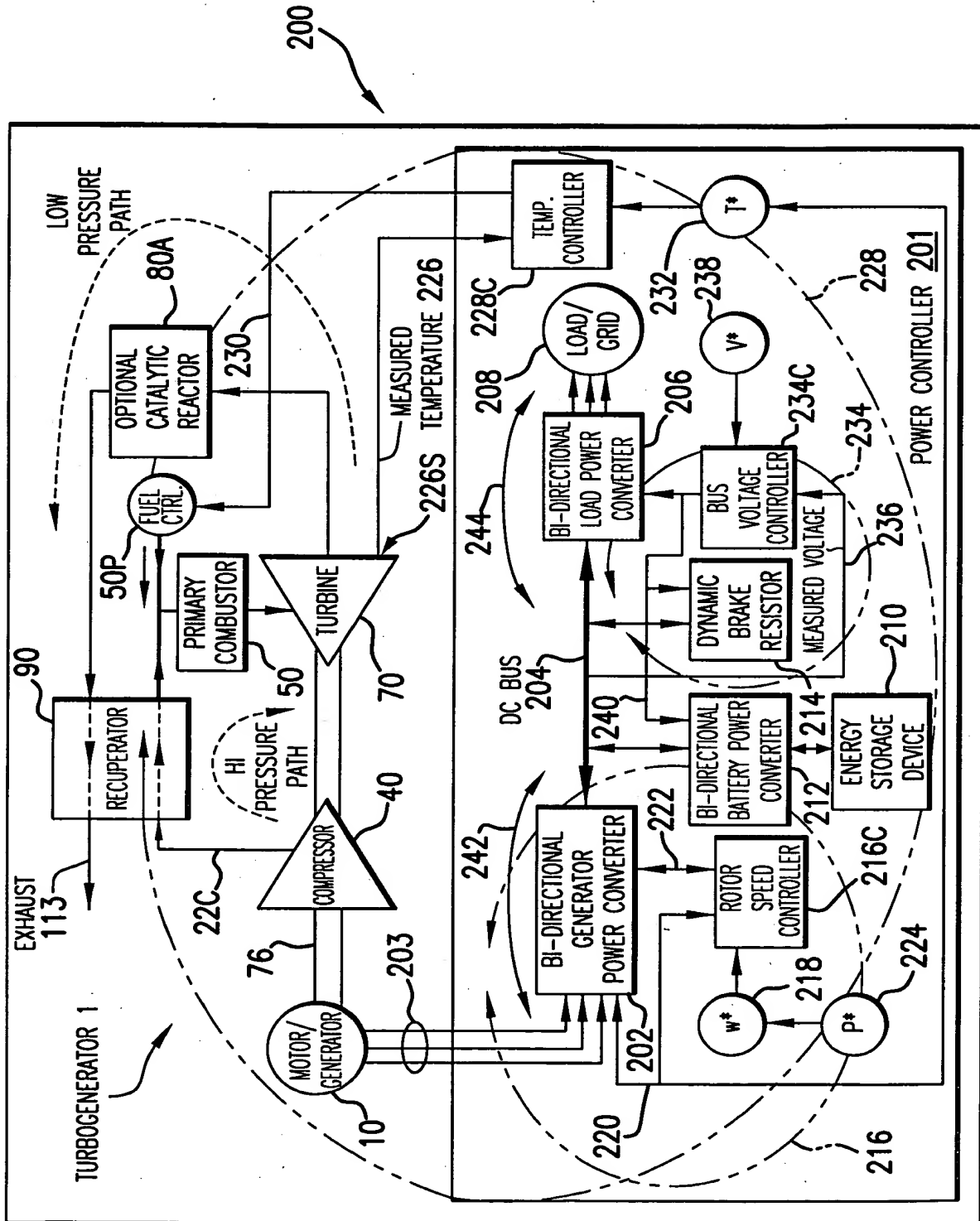


FIG.2

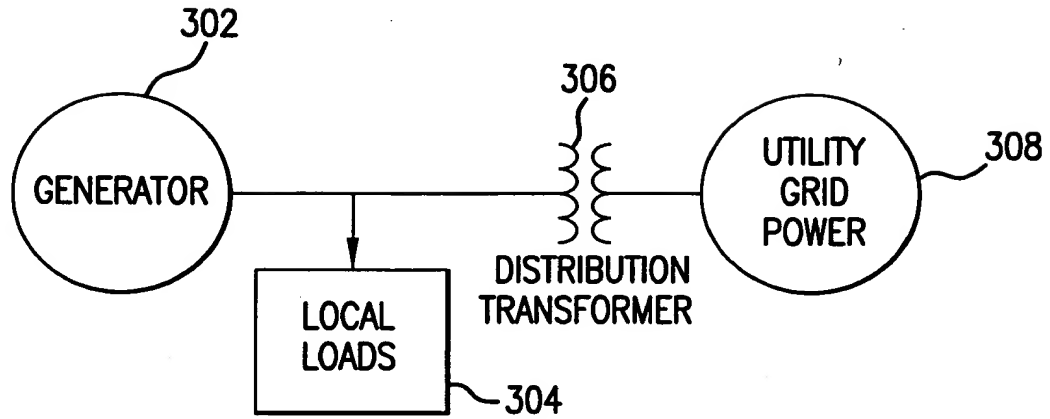


FIG.3

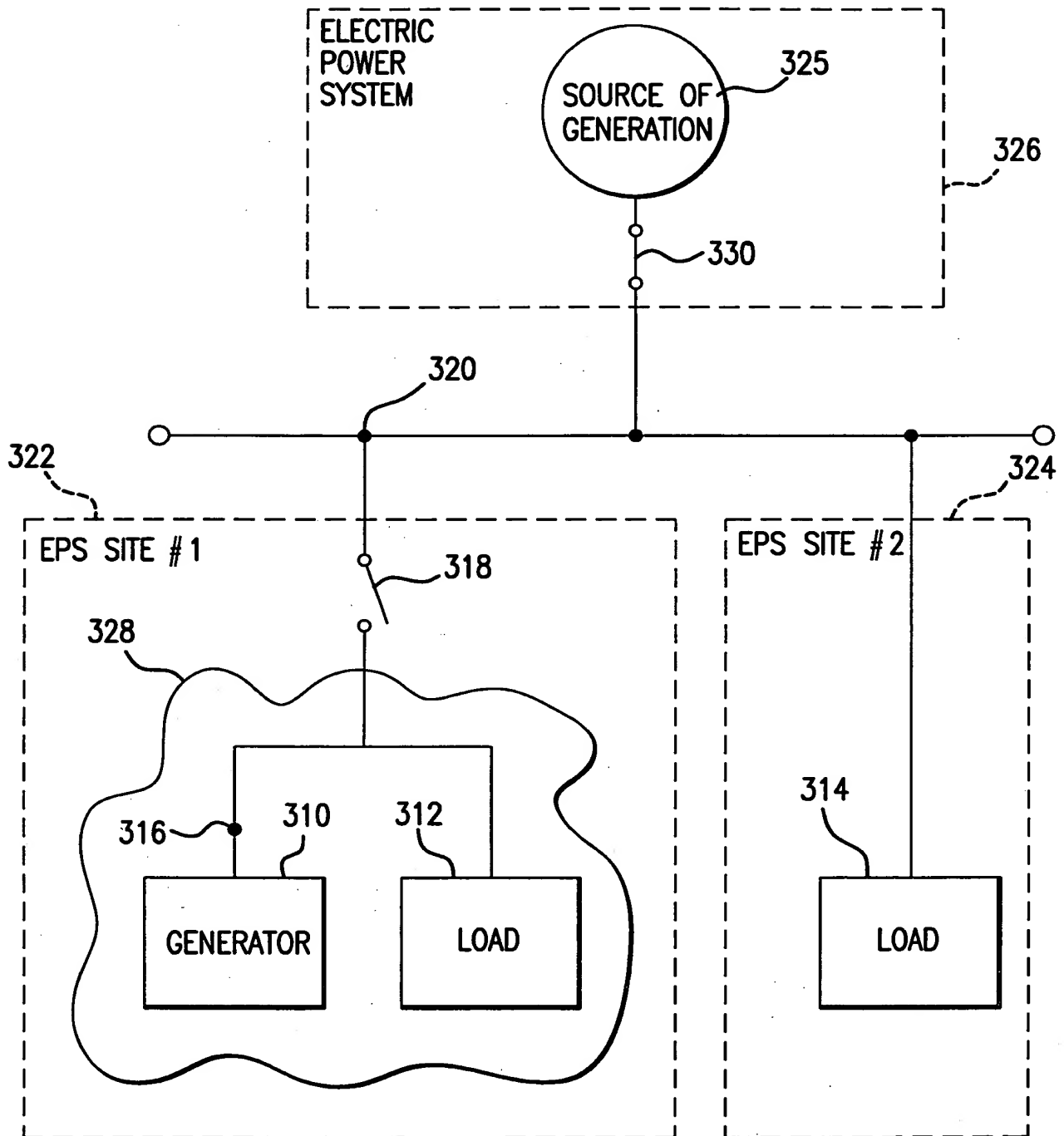


FIG.4

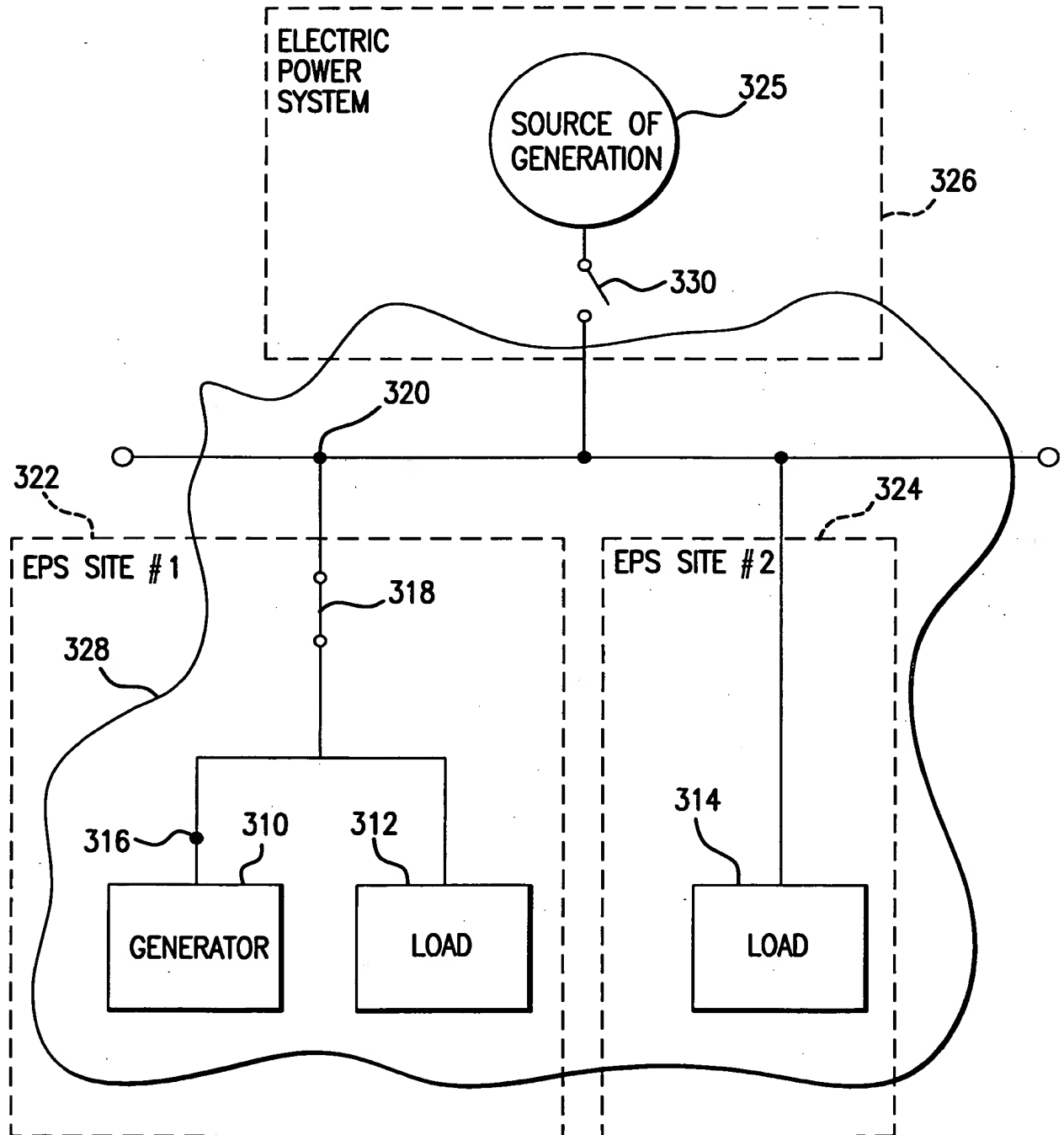


FIG.5

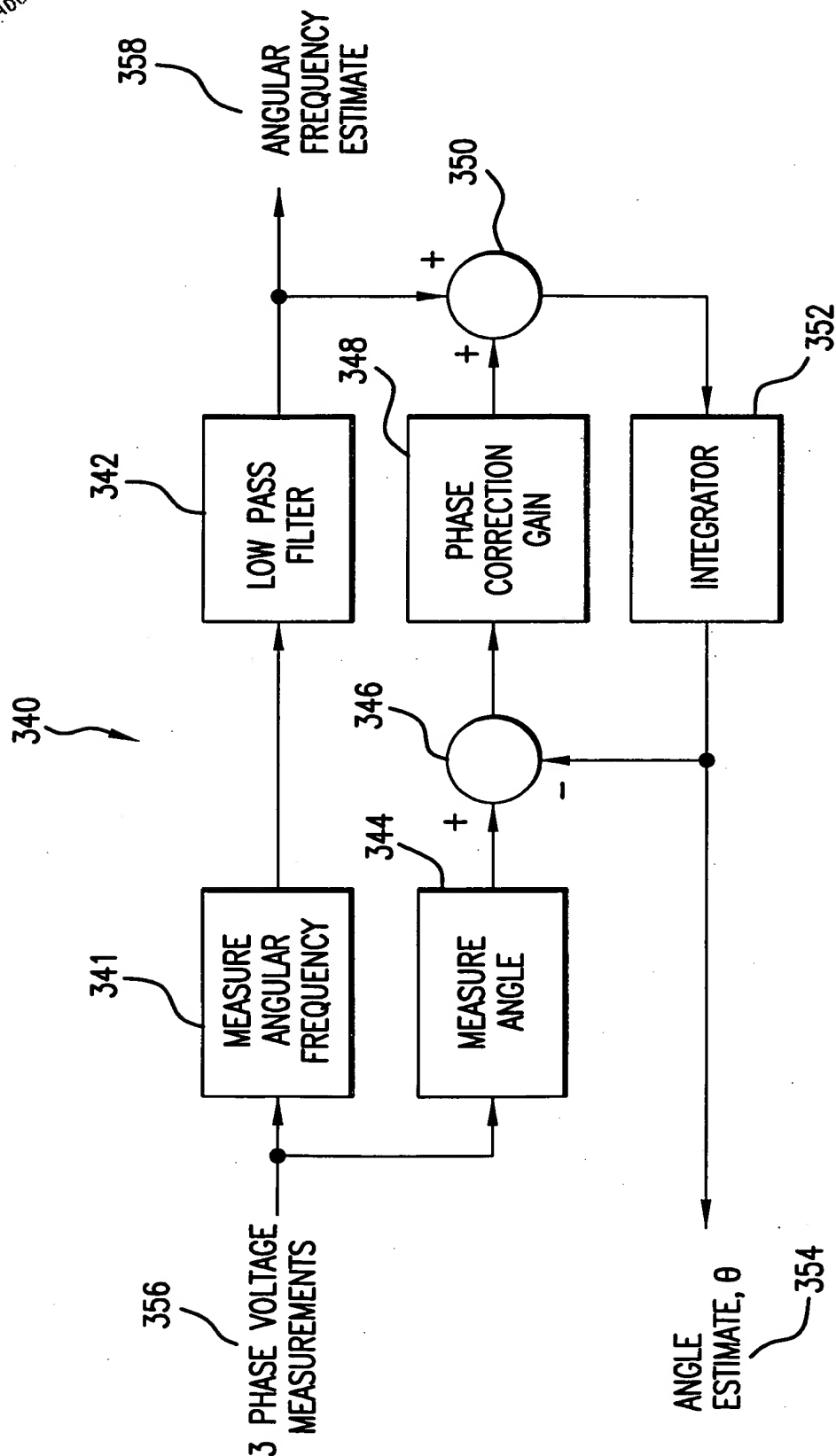
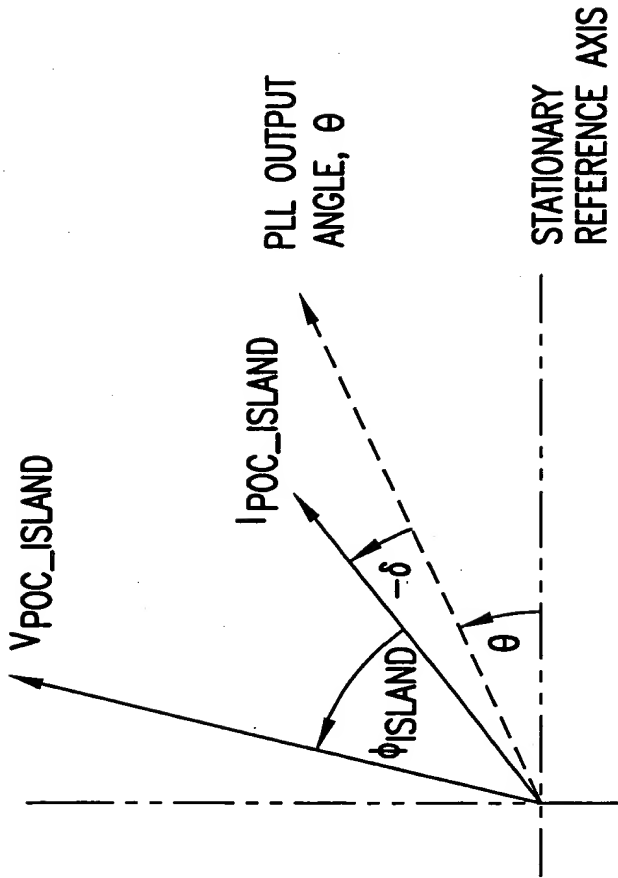


FIG. 6



I_{POC_ISLAND}	ISLANDED DR GENERATOR CURRENT MAGNITUDE AT THE POC (A)
V_{POC_ISLAND}	ISLANDED VOLTAGE MAGNITUDE AT THE POC (V)
Z_{ISLAND}	IMPEDANCE MAGNITUDE OF THE ISLAND LOOKING INTO THE POC (Ω)
ϕ_{ISLAND}	IMPEDANCE PHASE--ANGLE OF THE ISLAND LOOKING INTO THE POC (RADIAN)
P_{DRG}	DEMANDED REAL POWER OUTPUT OF THE DR GENERATOR (W)
Q_{DRG}	DEMANDED REACTIVE POWER OUTPUT OF THE DR GENERATOR (LAGGING IS POSITIVE) (VAR)
θ	ANGLE OUTPUT FROM THE PLL (RADIAN)
δ	DEMANDED CURRENT PHASE ANGLE, $\delta = \tan^{-1}(Q_{DRG}/P_{DRG})$ (RADIAN)

FIG.7

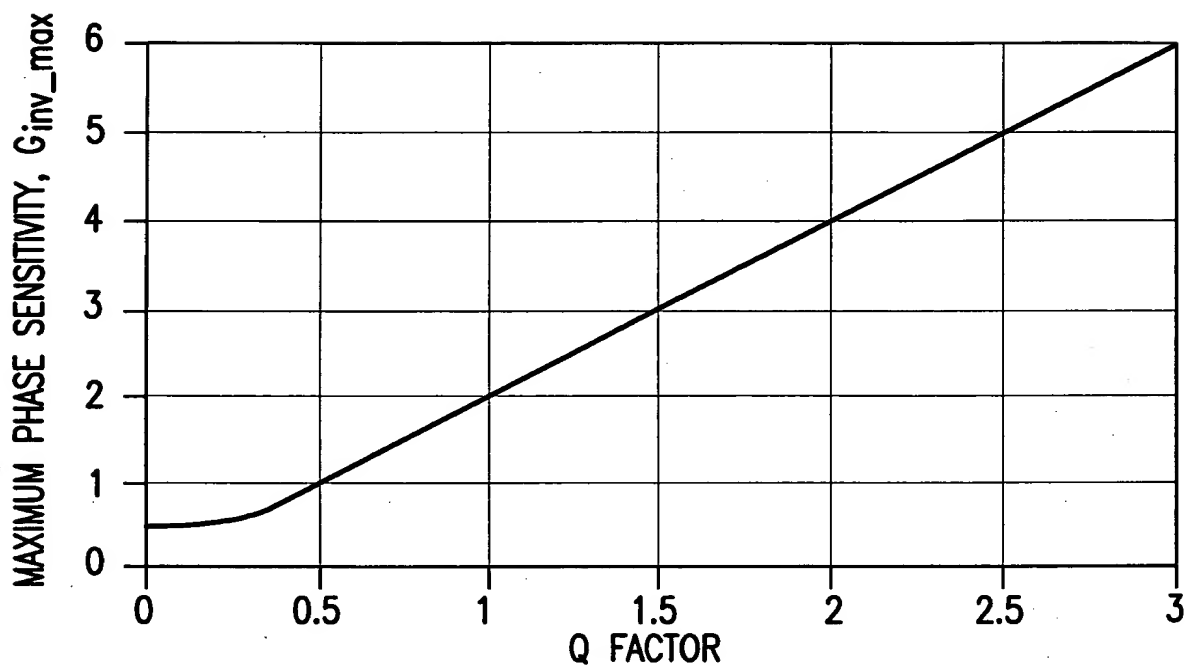


FIG.8

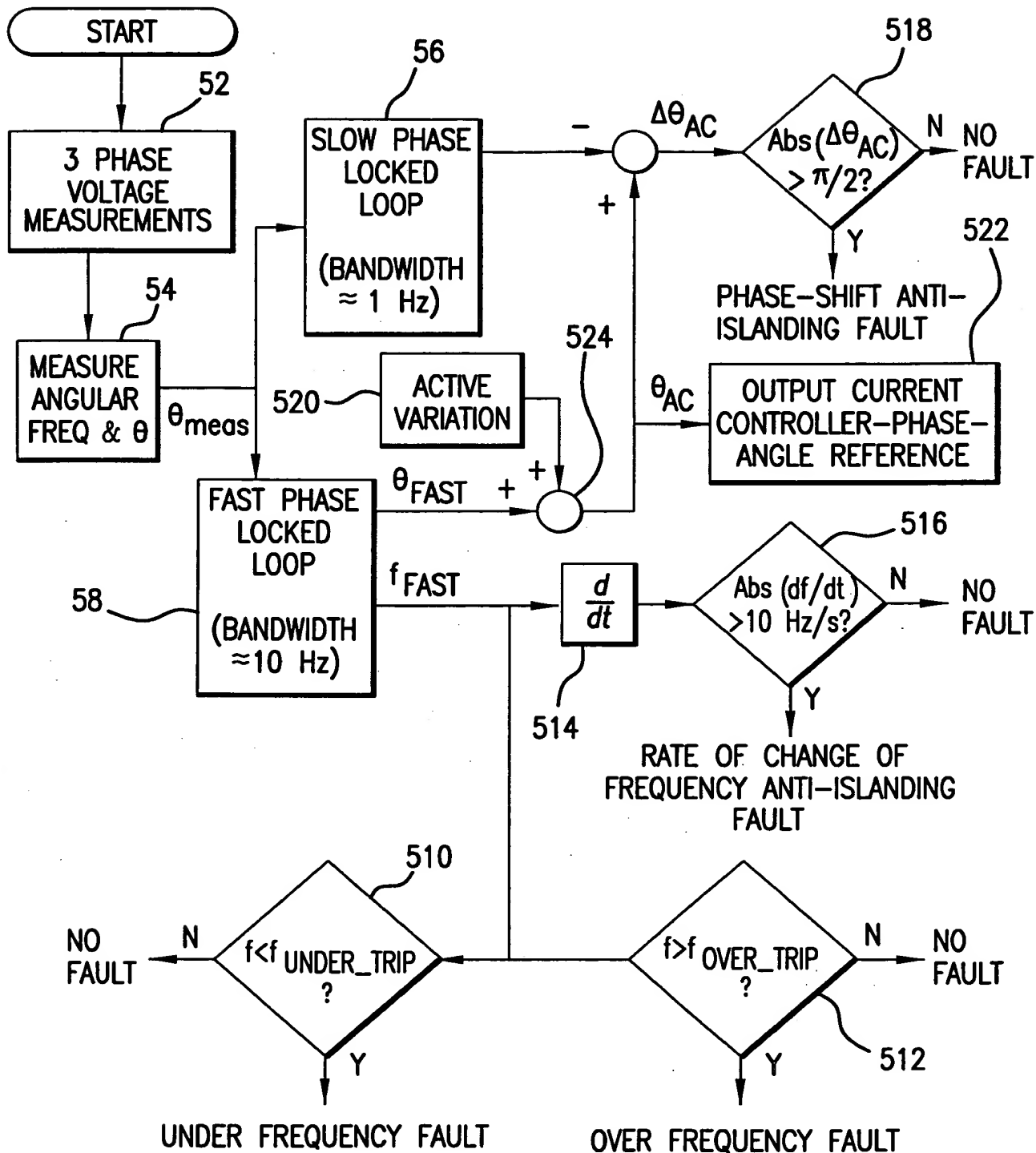


FIG.9A

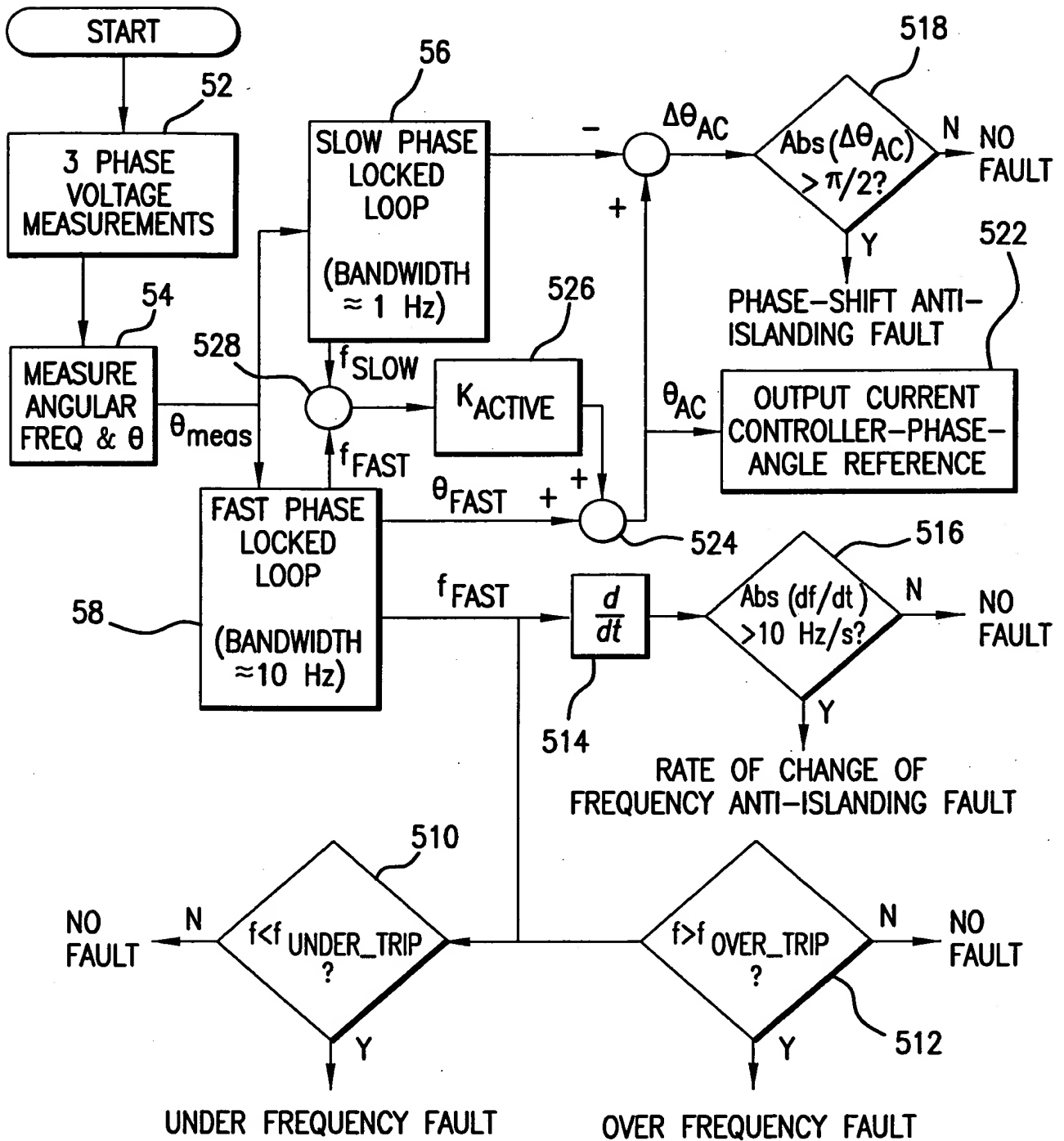


FIG.9B

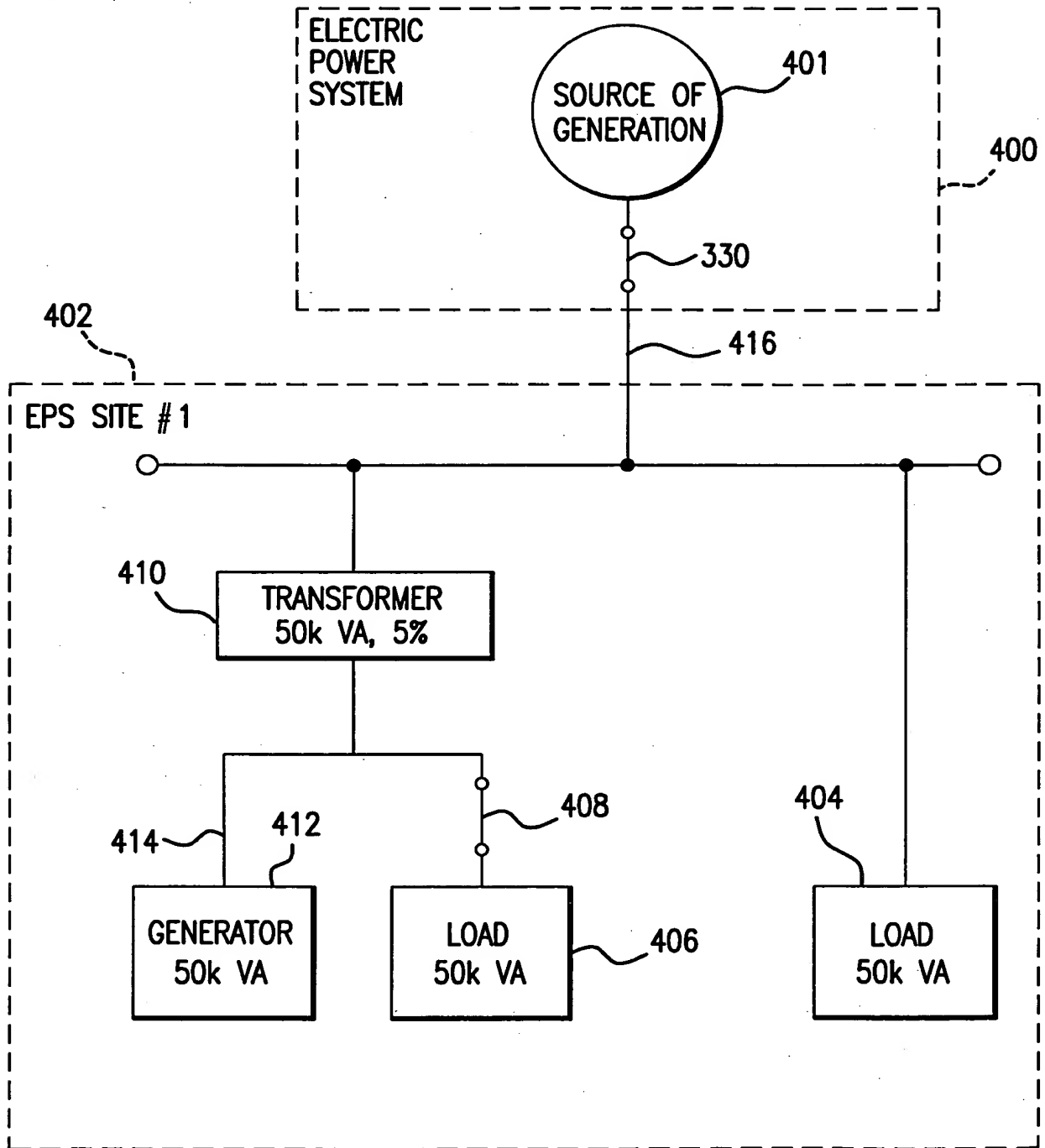


FIG.10